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```
=> s expandable (2w) address###
      21639 EXPANDABLE
      115733 ADDRESS###
L14      4 EXPANDABLE (2W) ADDRESS###
```

=> d 1-4 ti

```
US PAT NO:      4,602,330 [IMAGE AVAILABLE]      L14: 1 of 4
TITLE:          Data processor
```

```
US PAT NO:      4,566,082 [IMAGE AVAILABLE]      L14: 2 of 4
TITLE:          Memory pack addressing system
01 NOV 94 14:22:41      U.S. Patent & Trademark Office      P0020
```

```
US PAT NO:      3,839,707 [IMAGE AVAILABLE]      L14: 3 of 4
TITLE:          FAULT-ALARM AND CONTROL FOR A MICROWAVE COMMUNICATION NETWORK
```

```
US PAT NO:      3,805,247 [IMAGE AVAILABLE]      L14: 4 of 4
TITLE:          DESCRIPTION DRIVEN MICROPROGRAMMABLE MULTIPROCESSOR SYSTEM
```

```
=> s user interactive (p) compiler#
      180904 USER
      8706 INTERACTIVE
      180 USER INTERACTIVE
        (USER(W) INTERACTIVE)
      1880 COMPILER#
L15      0 USER INTERACTIVE (P) COMPILER#
```

```
=> s user interactive and compiler#
      180904 USER
      8706 INTERACTIVE
      8706 INTERACTIVE
      180 USER INTERACTIVE
        (USER(W) INTERACTIVE)
      1880 COMPILER#
L16      11 USER INTERACTIVE AND COMPILER#
```

=> d his

```
(FILE 'USPAT' ENTERED AT 12:59:57 ON 01 NOV 94)
L1      61 S C (2W) COMPILER AND (16 AND 32 AND 64)
L2      161 S COMPILER/AB,II
L3      7 S L2 AND 16 (2W) BIT# AND 32 (2W) BIT# AND 64 (2W) BIT#
L4      0 S CONVERTER (P) MACHINE LANGUAGE (P) DATA WIDTH (P) POINTER W
L5      0 S DATA WIDTH AND POINTER WIDTH
L6      6 S POINTER WIDTH
L7      7 S PARAMETER HOLDING MEANS
L8      819 S DESIGNATED (6W) USER
L9      46 S L8 AND COMPILER#
L10     19 S L9 AND WIDTH
        SET PAGELength 62
        SET LINELENGTH 78
L11     235 S USER (P) SELECT (P) WIDTH
L12     6 S L11 AND COMPILER
01 NOV 94 14:30:32      U.S. Patent & Trademark Office      P0021
L13     0 S EXPANDABLE (2W) ADDRESS### AND USER (2W) SELECT
L14     4 S EXPANDABLE (2W) ADDRESS###
L15     0 S USER INTERACTIVE (P) COMPILER#
L16     11 S USER INTERACTIVE AND COMPILER#
```

=>

INPUT: